

Assistant Professor / Professor in Cybersecurity

CDI de droit public

Job description: Assistant Professor/Professor in Cybersecurity at CentraleSupélec, in the IRISA laboratory (UMR CNRS 6074) for a full-time, permanent faculty position (CDI de droit public).

About CentraleSupélec

CentraleSupélec is a leading engineering school within the Paris-Saclay University. It is a prominent public institution (EPSCP in French) that operates under the authority of the French ministers for higher education and industry. CentraleSupélec mainly focuses on training high-level scientific general engineers, conducting research in engineering and systems sciences, and providing executive education.

The **CentraleSupélec campus in Rennes** provides courses for the three-year general engineering program, both in the FISA (Training of Engineers under Apprenticeship Status) and FISE (Training of Engineers under Student Status) tracks. CentraleSupélec provides a range of courses and programs dedicated to cybersecurity. These include the InfoSec course, the Cyber concentration within the general engineering program, and the specialized master's degree in CyberSecurity co-accredited with IMT Atlantique, as well as the University Research School (EUR) in Cybersecurity, Cyberschool.

IRISA is one of the largest research laboratories in France that specializes in computing and new information technologies. The laboratory is divided into seven scientific departments and focuses on research areas such as bioinformatics, cybersecurity, new software architectures, virtual reality, analysis of mass data, and artificial intelligence. IRISA has facilities in Rennes, Lannion, and Vannes, and operates within a thriving regional ecosystem of research and innovation. Cybersecurity is one of IRISA's eight transversal research axes and is central to the research of the laboratory's teams. The laboratory has widespread recognition both internationally and nationally in this field. It fosters strong collaborations with various state agencies (DGA-MI, ANSSI, ComCyber, etc.) and private entities operating in cybersecurity.

Teaching Responsibilities

The teaching responsibilities for this position amount to 192 hours (equivalent to tutorials), which can be allocated across various activities such as lectures, tutorials, practical sessions, projects, or overseeing teaching-related tasks. These teaching activities will cover a wide range of computer science subjects spanning both initial and continuing education programs. Ideally, candidates should demonstrate a keen interest in cybersecurity topics along with expertise in disciplines like software engineering, operating systems, networking, or web development. The candidate must be able to provide teaching in English. Specifically, the teaching responsibilities this position include the following:

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Campus de Rennes
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- Conduct lectures for students in the Specialized Engineering program “Cybersecurity”, throughout the three-year duration of the program. This will involve teaching various IT-related courses.
- Provide instruction to students enrolled in the CentraleSupélec General Engineering program, specifically targeting:
 - First-year students in the apprenticeship track.
 - First-year students in the "Networks and Security" track, which is part of the Engineering Science Program. These courses will be conducted at the Paris-Saclay campus.
 - Second and third-year students in the InfoSec course within the general curriculum
 - Third-year students enrolled in the ‘Cybersecurity’ major.
- Deliver courses to students enrolled in the Specialized Master’s program in Cybersecurity.

Research Responsibilities

Cybersecurity serves as the focal point for the two Inria/IRISA research teams, PIRAT and SUSHI, hosted on the Rennes campus of CentraleSupélec. These teams originated from the CIDRE research team at the IRISA Laboratory.

CentraleSupélec seeks to recruit a candidate to lead a cybersecurity research project aligned with the thematic areas of the Secure and Large-Scale Systems (D1 department) or the Architecture (D3 department) departments of IRISA. Preference will be given to applications proposing research projects aligned with the themes of the SUSHI and PIRAT teams. These themes encompass various aspects, such as attacks against information systems, system supervision, resilience of information systems, formal methods for low-level security, binary analysis, secure compilation, OS security, and (micro)architecture security.

Qualifications and Experience

The candidate must meet the following requirements:

- Hold a Ph.D. in computer science with contributions to Cybersecurity.
- Have published articles in international journals as an author or co-author. Specific publication requirements will vary based on the candidate’s curriculum vitae and years of experience.
- Show enthusiasm for teaching, conducting research (both academic and contractual industry-related), and teamwork.
- Be willing to supervise research work related to the themes of the IRISA Laboratory.

To qualify for the position of Professor, candidates must demonstrate experience in leading research activities, including projects, national and/or international research networks and assuming collective responsibilities.

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Application Process

Applications must be submitted by email to the following email address, drh.pole-enseignant@centralesupelec.fr, before April 30, 2024 with reference **MCF_IRISA_INFO_CYBERSECURITE_RENNES_2406**:

The electronic application must include the following PDF files:

- A cover letter
- A detailed CV (teaching experience, research, mobility, publications, etc.)
- A research and teaching project (five to ten pages) aligned with the objectives and focus of CentraleSupélec
- A copy of the identity card or passport
- A copy of the doctoral degree (for Assistant Professor)
- A copy of the HDR degree (postdoctoral accreditation for supervising research) is required for the Professor position application
- Thesis defense report or HDR report
- Letters of recommendation (optional)
- And any documents that attest to previous experience

Interview Process

Shortlisted candidates will be invited to an interview which consists of three stages, allowing us to assess your suitability for the position:

1. Candidates will present their academic background and present their teaching and research project.
2. Each candidate will demonstrate their teaching skills by presenting a lesson in English, addressing a common problem specified in the audition invitation.
3. Candidates will then respond to questions from the committee members.

The interview invitations will clearly state the duration for each of these presentations.

Scientific contacts

Guillaume Hiet, head of the SUSHI team guillaume.hiet@centralesupelec.fr

Valérie Viet Triem Tong, head of the PIRAT team valerie.viettriertong@centralesupelec.fr

Jean-François Lalande, head of Cyber courses Jean-Francois.Lalande@centralesupelec.fr

Yves Louet, Director of the Rennes campus Yves.louet@centralesupelec.fr

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